



PATENT
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Applicant(s): Chang-Whan Jung, *et al.*
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Title: FUSE CIRCUIT FOR SEMICONDUCTOR
INTEGRATED CIRCUIT

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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November 2, 2004
Date

Vanessa Marakas

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TRANSMITTAL LETTER

Sir:

Enclosed herewith for filing in the above-identified patent application please find the following:

1. Fee(s) Transmittal PTOL-85;
2. Check in the amount of \$1,700.00 to cover Issue Fee, ten (10) advance-order copies of the printed patent and the Publication Fee; and
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Respectfully submitted,

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